Project Part 4 description

Group Members: Logan Brown, Callum Duff, Jessica Larson

Project Description: Part four of the project involved doing the load and store instructions combined with an ADI for Rs. We did this by adding Rs as another input to the 16 bit mux. This allowed us to select the imm, imm + Rs, or Rs for what was being loaded or stored. Then two do to writes in one cycle we moved some of the logic from writeback into mem and then added the logic in writeback to have Rs <- Rs + imm. This was done similarly to the swap instruction of doing two writes in one cycle. Everything else stayed relatively the same, changes are noted in the ctrl file and the output sheet for the new instructions.